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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Izuo Iida  
Serial No. : 09/876,554  
Filed : June 7, 2001  
Title : METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE

Art Unit : 2815  
Examiner : N. Richards

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants submit the references listed on the attached form PTO-1449, copies of which are enclosed.

This statement is being filed after a first Office action on the merits, but before receipt of a final Office Action or a Notice of Allowance. I, the undersigned, hereby certify that each item of information contained in this statement was cited in a communication from a foreign patent office in a counterpart foreign application, the communication being dated April 25, 2003, which is not more than three months prior to the filing of this statement. Please apply any charges or credits to Deposit Account No. 06-1050.

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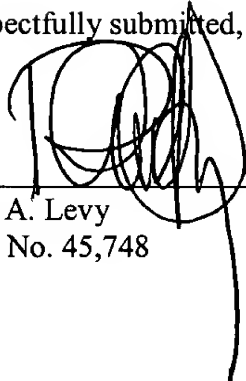
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Serial No. : 09/876,554  
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Page : 2 of 2

Attorney's Docket No.: 10417-084001 / F51-  
134741M/KIK

Respectfully submitted,

Date: 7/18/03

  
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